SMRITI GURUNG

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OBJECTIVE

Seeking an internship position in computer engineering field with special interest in digital system design.

EDUCATION

M.S., Computer Engineering, expected graduation May 2011 GPA: **3.25**
George Mason University, Fairfax, VA

B.S., Electronics and communication, May 2008 GPA: **3.5**
Delhi College of Engineering, New Delhi

RELEVANT COURSE WORK

Digital System Design with VHDL	Microprocessors	Computer Arithmetic
Analog Integrated Circuits	Digital Integrated Circuits	

WORK EXPERIENCE

Teaching Assistant, George Mason University, Fairfax, VA Spring 2010
Courses: Digital Logic Design with VHDL (lab and class)
Responsibilities include taking recitation, grading homework and exams and prepare question papers for undergraduate class. Help students with hands-on lab sessions.

Trainee Engineer /Research assistant **S**ummer 2007
Defense Research and Development Organization (DRDO), New Delhi, India
Designing and Testing of a 5-15kv high voltage dc (linear and switched mode) power supply for CO2 laser in LASTEC (Laser Technology) department of Defense Research & Development Organization (DRDO).

Labs Assistant, Delhi College Of Engineering, New Delhi, India Summer 2006
Troubleshooting personal computers, installing software and hardware installations.

- Complete software installs on Windows XP based systems.
- Performed hardware installations, diagnostic check, troubleshooting, and component repair.

TECHNICAL SKILLS

Programming Languages: VHDL, MATLAB, C/C++, Pspice
Programmable Hardware: Xilinx FPGA, Altera FPGA
Applications: MS Office, Aldec Active-HDL, Xilinx ISE, ModelSim, Cadence PSPICE, Microwind
Operating Systems: WINDOWS 2000/XP/VIST/7

RELEVANT ACADEMIC PROJECTS

- Implemented SHA-3 function Blue Midnight Wish in Xilinx optimized for area and speed implementation with automated control logic.
- Presented 8-bit microprocessor 8051 in VHDL with synthesis and implementation in Xilinx and performance description using different codes from open source.
- Designed and implemented CMOS based full adder circuit layout using Microwind.
- Microcontroller based home control management system.

AFFILIATION

Member, Cryptography Engineering and Research Group, George Mason University.